

SANDEEP USHKEWAR

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EDUCATIONAL PROFILE

- 2017** **M. Tech (Electrical specialization with Power Electronics & Power System)** from Sardar Patel College of Engineering, Andheri (W), Mumbai **with CPI 9.56**
- 2013** **B. Tech (Electrical Engineering)** from **VJTI**, Matunga (E), Mumbai **with CPI 7.50**
- 2010** **Diploma in Electrical Engineering** from Government Polytechnic, Nanded.
And secured first rank in college with an aggregate of **88.25%**
- 2007** **SSC** from Mahatma Phule High school, Nanded. And scored **84.15 %**

WORK EXPERIENCE – Lecturer & Assistant Professor (Electrical Engineering)

Organization: Veermata Jijabai Technological Institute (**VJTI, Mumbai**)

Period: July'2013– June'2015 (**2 years**)

July'2017– May'2018 (**1 year**)

Currently working as an Assistant professor in Electrical Engineering Department, SVKMs Institute of Technology, Dhule on probation basis.

B.TECH PROJECT & M.TECH DISSERTATION

B. Tech Project-

"Controlled Islanding Scheme for Power System Protection: Guidelines and Approach (Case Study: Proposed Bhopal Islanding Scheme)" project under POSOCO, Western Regional Load Despatch Centre (WRLDC) Mumbai. Published an IEEE conference paper based on the project.

M. Tech Dissertation-

"Design & Fabrication of Split Core Pulse Transformer for Prototype Solid State Pulse Power Modulator"

ACHIEVEMENTS

- Received **M.Tech meritorious student award twice** from Sardar Patel College of Engineering, Mumbai on its 55th & 56th Annual Day (March 2017 & 2018 respectively).
- Received Scholarship from **Priyadarshani Academy, Mumbai** in final Year B. Tech of Electrical Engineering, VJTI Mumbai (2013).

IEEE PUBLICATION DETAILS

1. **Sandeep Ushkewar**, V. Shinde and V. Dake, "Modeling and analysis of split core pulse transformer for solid state pulse power modulator," *2017 Second International Conference on Electrical, Computer and Communication Technologies (ICECCT)*, Coimbatore, 2017, pp.1-6.
doi: 10.1109/ICECCT.2017.8118032
2. **Sandeep Ushkewar**, V. Dake and V. Shinde, "Designing of solid state pulse power modulator by fabrication of pulse transformer," *2017 Innovations in Power and Advanced Computing Technologies (i-PACT)*, Vellore, 2017, pp.1-5.
doi: 10.1109/IPACT.2017.8244942
3. **Sandeep Ushkewar**, S. Chaube, N. Komawar, N. Tirpude and M. S. Ansari, "Controlled islanding scheme for power system protection: Guidelines and approach: Case study: Proposed Bhopal islanding scheme," *2017 Third International Conference on Advances in Electrical, Electronics, Information, Communication & Bio-Informs (AEEICB)*, Chennai, 2017, pp.117-121.
doi: 10.1109/AEEICB.2017.7972394
4. **Sandeep Ushkewar**, Mahesh Bodke, "Compensation of Reactive Power in DFIG Based Wind Farm using STATCOM," *2018 Fourth International Conference on Advances in Electrical, Electronics, Information, Communication and Bio-Informatics (AEEICB)*, Chennai, 2018.
5. Anant Dhulshette and **Sandeep Ushkewar**, "Validation Of STATCOM Capabilities through Modelling And Simulation On Simulink Platform," *2018 Fourth International Conference on Advances in Electrical, Electronics, Information, Communication and Bio-Informatics (AEEICB)*, Chennai, 2018.
6. Nilesh Ahire, Megha Fernandese and **Sandeep Ushkewar**, "Optimal Allocation Of Shunt Compensation And Maximum Loadability Assessment On IEEE-14 Bus Test System," *2018 Fourth International Conference on Advances in Electrical, Electronics, Information, Communication and Bio-Informatics (AEEICB)*, Chennai, 2018. **(Accepted and Presented in FEB18)**

National Journal (UGC Approved)

7. **Sandeep Ushkewar** and Nilesh Ahire, "Design & Fabrication of Split Core Pulse Transformer on Hardware Platform," *Indian Journal of Technical Education (IJTE)* 2018. **(Communicated)**

International Journal (UGC Approved)

8. Nilesh R. Ahire and **Sandeep Ushkewar**, "Implementation of P&O MPPT Method Using ARDUINO Controller for A Standalone Solar PV System," *International Journal of Electronics, Electrical and Computer Systems (IJEECS)* Volume 7, Issue 03, March 2018 |ISSN (online): 2348-117X.
9. Sandeep Ushkewar, "DC load flow studies of DC link for power system planning," *International Journal of creative research thoughts (IJCRT)* Volume 6, Issue 3, April 2018 |ISSN (online): 2320-2882.

PERSONAL INFORMATION

- Date of Birth : 11th October, 1992
- Father name : Mr. Sunil Buchanna Ushkewar
- Nationality : Indian
- Languages : Marathi (Mother tongue), English, Hindi and Gujarati.
- Permanent address : Agnipankh Niwas No.1-7-1448, Naik nagar, Nanded MH- 431 605.